

## **AMENDMENTS TO THE SPECIFICATION**

**The following amended paragraph replaces the third full paragraph, beginning on line 9, on page 1 of the specification:**

Systems, for complex computational tasks, such as radar, sonar and signal processing and signal intelligence often rely upon a number of processors which must be interconnected for tasks such as data communication, memory sharing and distributed processing. Multiple processor boards, such as the CHAMP-AV illustrated in Figure 8 and the CHAMP-AV II illustrated in Figure 9, manufactured by DY4 Systems are often used to achieve higher processing capacity. Some applications require the implementation of several multiple processor boards. Often a bus structure with a separate processor for bus traffic control is implemented for interconnection of multiple processors. A traffic managed ~~buss~~ bus requires a dedicated active backplane for signal transfer and dedicated control resources. Dedicated switches are typically implemented on dedicated switch cards. A managed ~~buss~~ bus is not fully scalable and the speed of a managed buss will decrease with the addition of resources.

**The following amended paragraph replaces the paragraph, beginning on line 20, on page 5 of the specification:**

The PMC adaptor provides both the bridge and a switch. ~~A interconnected~~ An interconnected DSP system is constructed solely of these components, with associated interconnecting wiring or backplane. There are no active backplane overlay modules, active hubs, or special cards required. As a result, the logistics costs of maintenance and sparing are the minimum possible. During a development project, reconfiguring the system requires little more than re-arranging standard category-5 cables and re-initializing the software to perform the network discovery process.